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**PATENT NUMBER and  
ISSUE DATE**

## U.S. UTILITY Patent Application

|  |                           |                                    |                 |             |                      |
|--|---------------------------|------------------------------------|-----------------|-------------|----------------------|
| APPL NUM<br>10067486   | FILING DATE<br>02/04/2002 | CLASS<br>257                       | SUBCLASS<br>655 | GAU<br>2822 | EXAMINER<br>K. DUONG |
| <b>**APPLICANTS:</b> Tayanaka Hiroshi;   |                           |                                    |                 |             |                      |
| <b>**CONTINUING DATA VERIFIED:</b> Yes<br>THIS APPLICATION IS A CON OF 09/217,907 12/22/1998<br><i>KED</i>   |                           |                                    |                 |             |                      |
| BEST AVAILABLE COPY  |                           |                                    |                 |             |                      |
| <b>** FOREIGN APPLICATIONS VERIFIED:</b> Yes<br>JAPAN P09-360429 12/26/1997<br><i>KED</i>  |                           |                                    |                 |             |                      |
| PG-PUB <input checked="" type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>   |                           | RESCIND <input type="checkbox"/>   |                 |             |                      |
| Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no<br>35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no<br>Verified and Acknowledged Examiners's initials <i>KED</i> |                           | ATTORNEY DOCKET NO<br>9792909-5361 |                 |             |                      |
| <b>TITLE :</b> Semiconductor substrate and thin-film semiconductive member, and method for making thereof  |                           |                                    |                 |             |                      |

|  |                  |                           |                             |                             |                   |
|--|------------------|---------------------------|-----------------------------|-----------------------------|-------------------|
| <b>NOTICE OF ALLOWANCE MAILED</b>  |                  | <b>Assistant Examiner</b> | <b>CLAIMS ALLOWED</b>       |                             |                   |
|  |                  |                           | <b>Total Claims</b>         | <b>Print Claim for O.G.</b> |                   |
|  |                  |                           |                             |                             |                   |
| <b>ISSUE FEE</b>   |                  | <b>Primary Examiner</b>   | <b>DRAWING</b>              |                             |                   |
| <b>Amount Due</b>  | <b>Date Paid</b> |                           | <b>Sheets Drwg.</b>         | <b>Figs.Drwg.</b>           | <b>Print Fig.</b> |
|  |                  |                           |                             |                             |                   |
| <input type="checkbox"/> <b>TERMINAL</b><br><b>DISCLAIMER</b>  |                  | <b>PREPARED FOR ISSUE</b> | <b>Application Examiner</b> |                             |                   |
| <b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only. |                  |                           |                             |                             |                   |

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